SFP-25G-SR-AR-PRO
Arista Networks ${ }^{\circledR}$ SFP-25G-SR Compatible TAA Compliant 25GBase-SR SFP28 Transceiver (MMF, 850nm, 100m, DOM, 0 to 70C, LC)

## Features

- SFF-8402 and SFF-8472 Compliance
- Duplex LC Connector
- Commercial Temperature 0 to 70 Celsius
- Multi-mode Fiber
- Hot Pluggable
- Excellent ESD Protection
- Metal with Lower EMI
- RoHS Compliant and Lead Free



## Applications:

- 25GBase Ethernet
- Access and Enterprise


## Product Description

This Arista Networks ${ }^{\circledR}$ SFP-25G-SR compatible SFP28 transceiver provides 25GBase-SR throughput up to 100 m over multi-mode fiber (MMF) using a wavelength of 850 nm via an LC connector. It is guaranteed to be $100 \%$ compatible with the equivalent Arista Networks ${ }^{\circledR}$ transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. Digital optical monitoring (DOM) support is also present to allow access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

Proline's transceivers are RoHS compliant and lead-free.

TAA refers to the Trade Agreements Act (19 U.S.C. \& 2501-2581), which is intended to foster fair and open international trade. TAA requires that the U.S. Government may acquire only "U.S. - made or designated country end products.


Absolute Maximum Ratings

| Parameter | Symbol | Min. | Typ. | Max |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
| Storage Temperature | Ts | -40 |  | 85 |  |
| Relative Humidity | RH | 5 |  | 95 |  |
| Supply Voltage | Vcc | -0.5 |  | 0 |  |
| Operating Case Temperature | Tc | 0 |  | 4.0 | V |

Electrical Characteristics

| Parameter |  | Symbol | Min | Typ | Max | Unit | Notes |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply Voltage |  | Vcc | 3.135 | 3.3 | 3.465 | V |  |
| Data Rate |  |  |  | 25.78 |  | GB/s |  |
| Module Supply Current |  | Icc |  |  | 290 | mA |  |
| Power Dissipation |  | $\mathrm{P}_{\mathrm{D}}$ |  |  | 1000 | mW |  |
| Transmitter |  |  |  |  |  |  |  |
| Input Differential Impedance |  | $\mathrm{Z}_{\text {IN }}$ |  | 100 |  | $\Omega$ |  |
| Differential Data Input Swing |  | $\mathrm{V}_{\text {IN, P-P }}$ | 180 |  | 700 | $\mathrm{m} \mathrm{V}_{\mathrm{p}-\mathrm{p}}$ |  |
| TX_FAULT | Transmitter Fault | $\mathrm{V}_{\mathrm{OH}}$ | 2.0 |  | $\mathrm{V}_{\text {cc }}$ | V | TX_FAULT |
|  | Normal Operation | $\mathrm{V}_{\text {oL }}$ | 0 |  | 0.8 | V |  |
| TX_DISABLE | Transmitter Disable | $\mathrm{V}_{\text {IH }}$ | 2.0 |  | $\mathrm{V}_{\text {cc }}$ | V | TX_DISABLE |
|  | Transmitter Enable | VIL | 0 |  | 0.8 | V |  |
| Receiver |  |  |  |  |  |  |  |
| Output Differential Impedance |  | $\mathrm{Z}_{0}$ |  | 100 |  | $\Omega$ |  |
| Differential Data Output Swing |  | $V_{\text {out, p-p }}$ | 300 |  | 850 | $\mathrm{m} \mathrm{V}_{\mathrm{p}-\mathrm{p}}$ | 1 |
| Data Output Rinse Time, Fall Time |  | $\mathrm{tr}, \mathrm{tf}$ |  | 30 |  | Ps | 2 |
| Rx_LOS | Loss of Signal (LOS) | $\mathrm{V}_{\mathrm{OH}}$ | 2.0 |  | VCC | V | RX_LOS |
|  | Normal Operation | VoL | 0 |  | 0.8 | V |  |

## Notes:

1. Internally AC coupled, but requires a external $100 \Omega$ differential load termination.
2. $20-80 \%$
3. LOS is an open collector output. Should be pulled up with $4.7 \Omega$ on the host board.

Optical Characteristics

| Parameter | Symbol | Min | Typ | Max | Unit | Notes |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Transmitter |  |  |  |  |  |  |
| Launch Optical Power | Po | -7.6 |  | +2.4 | dBm | 1 |
| Extinction Ratio | ER | 2 |  |  | dB |  |
| Center Wavelength Range | $\lambda c$ | 840 | 850 | 860 | nm |  |
| Transmitter Dispersion Penalty @25.78GB/s | TWDP |  |  | 5 | dB |  |
| Spectral Width (RMS) @25.78Gb/s | $\Delta \lambda$ |  |  | 0.6 | nm |  |
| Optical Return Loss Tolerance | ORLT |  |  | 12 | dB |  |
| Pout @TX-Disable Asserted | Poff |  |  | -30 | dBm | 1 |
| Receiver |  |  |  |  |  |  |
| Center Wavelength | $\lambda c$ | 840 |  | 860 | nm |  |
| Receiver Sensitivity (P avg) | S |  |  | -11 | dBm | 2 |
| Receiver Overload (P avg) | Pol | 2.5 |  |  | dBm |  |
| Optical Return Loss | ORL | 12 |  |  | dB |  |
| LOS De-Assert | LOS $_{\text {D }}$ |  |  | -12 | dBm |  |
| LOS Assert | LOS $_{\text {A }}$ | -30 |  |  | dBm |  |
| LOS Hysteresis |  | 0.5 |  |  | dB |  |

## Notes:

1. $50 / 125 \mu \mathrm{~m}$ fiber with $\mathrm{NA}=0.2,62.5 / 125 \mu \mathrm{~m}$ fiber with $\mathrm{NA}=0.275$.
2. Measured with PRBS 231-1 at 10-4 BER @25.78Gb/s.

Pin Descriptions

| Pin | Symbol | Name/Descriptions | Ref. |
| :--- | :--- | :--- | :--- | :--- |
| $\mathbf{1}$ | VeeT | Transmitter Ground | 1 |
| $\mathbf{2}$ | TX Fault | Transmitter Fault (LVTTL-O) - High indicates a fault condition | 2 |
| $\mathbf{3}$ | SDA | Transmitter Disable (LVTTL-I) - High or open disables the <br> transmitter | 3 |
| $\mathbf{4}$ | SCL | Two wire serial interface Data Line (LVCMOS-I/O) (MOD-DEF2) | 4 |
| $\mathbf{5}$ | Two wire serial interface Clock Line (LVCMOS-I/O) (MOD- | 4 |  |
| $\mathbf{6}$ | REF1) |  |  |

## Notes:

1. The module signal grounds are isolated from the module case.
2. This is an open collector/drain output that on the hostboad requires a $4.7 \mathrm{~K} \Omega$ to $10 \mathrm{~K} \Omega$ pull-up resistor to VccHost.
3. This input is internally biased high with a $4.7 \mathrm{~K} \Omega$ to $10 \mathrm{~K} \Omega$ pull-up resistor to VccT.
4. Two-Wire Serial interface clock and data lines require an external pull-up resistor dependent on the capacitance load.
5. This is a ground return that on the host board requires a $4.7 \mathrm{~K} \Omega$ to $10 \mathrm{~K} \Omega$ pull-up resistor to VccHost.
6. Rate select can also be set through the 2-wire bus in accordance with SFF-8472 v. 10.2, Rx Rate Select is set at Bit 3, Byte 110.
Address A2h. Tx Rate Select is set at Bit 3, Byte 118, Address A2h.
Note: writing a " 1 " selects maximum bandwidth operation. Rate select is the logic OR of the input state of Rate Select Pin and 2-wire bus.

Host PCB SFP+ pad assignment top view


Recommended Host Board Power Supply Filter Network


## Recommended Application Interface Block Diagram



Mechanical Specifications


Unit:mm


## About Us:

Proline Options is one of North America's leading providers of transceivers and high speed cabling. With a reputation for quality, tested products that cover the connectivity spectrum, Proline Options has a solution for you regardless of the specification.

At Proline Options, every product is tested in its intended application - never batch or spec tested only. We run bandwidth, distance and IOS network tests. We have documented an impressive $0.03 \%$ failure rate over the last 10 years. To continue this rate of success we invest millions annually in our own on-site testing lab.

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