

## FET-40G-PRO

Cisco® FET-40G Compatible TAA Compliant 40GBase-SR4 QSFP+ Transceiver (MMF, 850nm, 100m, DOM, 0 to 70C, MPO)

### Features

- SFF-8436 Compliance
- MPO Connector
- Commercial Temperature 0 to 70 Celsius
- Multi-mode Fiber
- Hot Pluggable
- Excellent ESD Protection
- Metal with Lower EMI
- RoHS Compliant and Lead Free



### Applications:

- 40GBase Ethernet
- Access and Enterprise

### Product Description

This Cisco® FET-40G compatible QSFP+ transceiver provides 40GBase-SR4 throughput up to 100m over multi-mode fiber (MMF) using a wavelength of 850nm via an MPO connector. It is guaranteed to be 100% compatible with the equivalent Cisco® transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. Digital optical monitoring (DOM) support is also present to allow access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

Proline's transceivers are RoHS compliant and lead-free.

TAA refers to the Trade Agreements Act (19 U.S.C. & 2501-2581), which is intended to foster fair and open international trade. TAA requires that the U.S. Government may acquire only "U.S. – made or designated country end products.



## Absolute Maximum Ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit
Maximum Supply Voltage	Vcc	-0.5		4.0	°C
Storage Temperature	TS	-40		85	°C
Operating Case Temperature	TC	0	25	70	°C
Relative Humidity	RH	5		95	%
Data Rate Per Channel			10.3125		Gbps

## Electrical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Supply Voltage	Vcc	3.135	3.3	3.465	V	
Module Supply Current	Icc			430	mA	
Power Dissipation	PD			1.5	W	
<b>Transmitter</b>						
Input Differential Impedance	Zin		100		Ω	
Differential Data Input Swing	Vin, p-p	180		900	mVp-p	
<b>Receiver</b>						
Output Differential Impedance	Zo		100		Ω	
Differential Data Output Swing	Vout, p-p	300		850	mVp-p	1
Data Output Rise Time, Fall Time	tr, tf	28			ps	2

### Notes:

1. Internally AC coupled, but requires a external 100Ω differential load termination.
2. 20 – 80 %.

## Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
<b>Transmitter</b>						
Launch Optical Power	Po	-7.6		+2.4	dBm	1
Center Wavelength Range	$\lambda_c$	830	850	860	nm	
Extinction Ratio	EX	3			dB	2
Spectral width (RMS)	$\Delta\lambda$			0.65	nm	
Transmitter and Dispersion Penalty	TDP			3.2	dB	
Optical Return Loss Tolerance	ORLT			12	dB	
Eye Diagram		IEEE Std 802.3ba compatible				
<b>Receiver</b>						
Center Wavelength	$\lambda_c$	830	850	860	nm	
Receiver Sensitivity (Pavg)	S			-9.5	dBm	3
Damage Threshold	Pol	2.5			dBm	3
Optical Return Loss	ORL	12			dB	
LOS De-Assert	LOSD			-11	dBm	
LOS Assert	LOSA	-30			dBm	
LOS Hysteresis		0.5			dB	

### Notes:

1. The optical power is launched into OM3 MMF.
2. Measured with a PRBS  $2^{31}-1$  test pattern @10.3125Gbps.
3. Measured with PRBS  $2^{31}-1$  test pattern, 10.3125Gb/s, BER<10<sup>-12</sup>

## Pin Descriptions

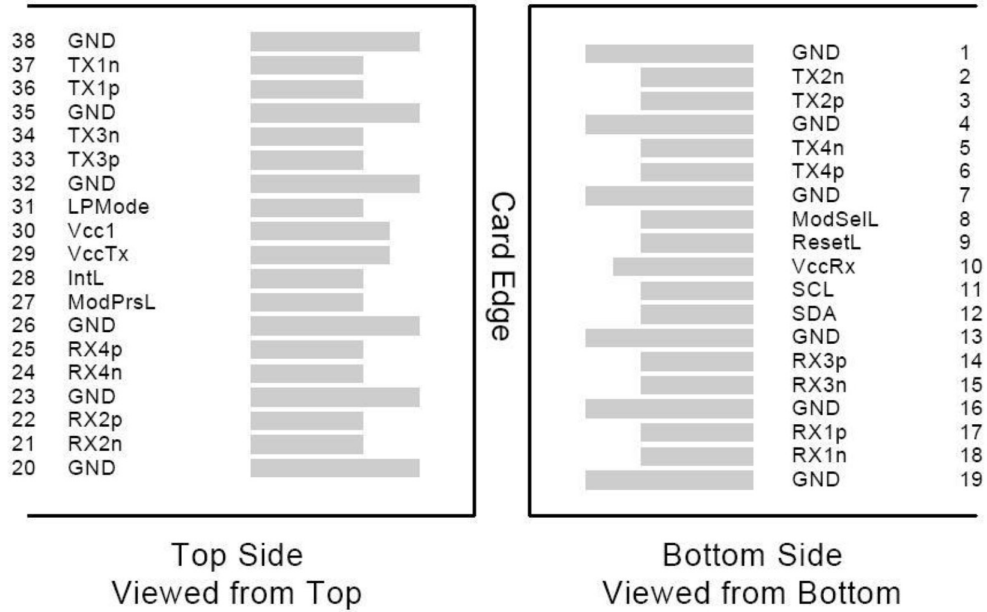
Pin	Logic	Symbol	Name/Descriptions	Ref.
1		GND	Module Ground	1
2	CML-I	Tx2-	Transmitter inverted data input	
3	CML-I	Tx2+	Transmitter non-inverted data input	
4		GND	Module Ground	1
5	CML-I	Tx4-	Transmitter inverted data input	
6	CML-I	Tx4+	Transmitter non-inverted data input	
7		GND	Module Ground	1
8	LVTTTL-I	MODSEIL	Module Select	2
9	LVTTTL-I	ResetL	Module Reset	2
10		VCCR <sub>x</sub>	+3.3v Receiver Power Supply	
11	LVC MOS-I	SCL	2-wire Serial interface clock	2
12	LVC MOS-I/O	SDA	2-wire Serial interface data	2
13		GND	Module Ground	1
14	CML-O	RX3+	Receiver non-inverted data output	
15	CML-O	RX3-	Receiver inverted data output	
16		GND	Module Ground	1
17	CML-O	RX1+	Receiver non-inverted data output	
18	CML-O	RX1-	Receiver inverted data output	
19		GND	Module Ground	1
20		GND	Module Ground	1
21	CML-O	RX2-	Receiver inverted data output	
22	CML-O	RX2+	Receiver non-inverted data output	
23		GND	Module Ground	1
24	CML-O	RX4-	Receiver inverted data output	
25	CML-O	RX4+	Receiver non-inverted data output	
26		GND	Module Ground	1
27	LVTTTL-O	ModPrsL	Module Present, internal pulled down to GND	
28	LVTTTL-O	IntL	Interrupt output, should be pulled up on host board	2
29		VCCT <sub>x</sub>	+3.3v Transmitter Power Supply	
30		VCC1	+3.3v Power Supply	
31	LVTTTL-I	LPMode	Low Power Mode	2
32		GND	Module Ground	1
33	CML-I	Tx3+	Transmitter non-inverted data input	

34	CML-I	Tx3-	Transmitter inverted data input	
35		GND	Module Ground	1
36	CML-I	Tx1+	Transmitter non-inverted data input	
37	CML-I	Tx1-	Transmitter inverted data input	
38		GND	Module Ground	1

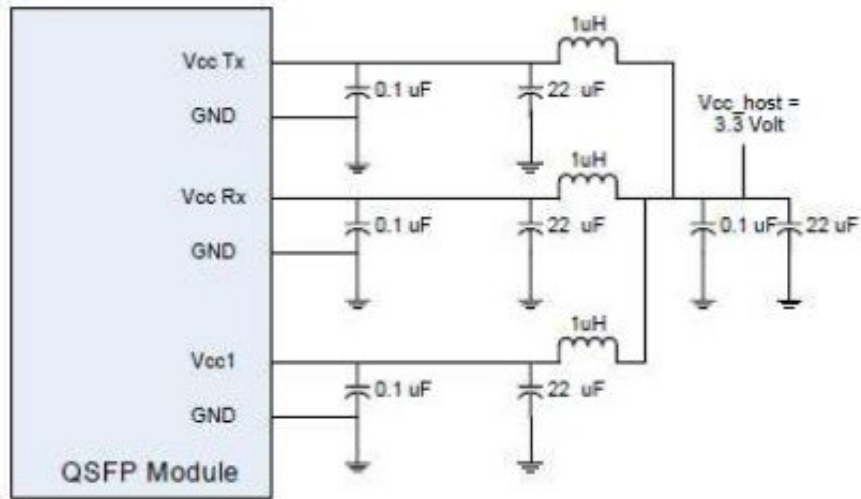
**Notes:**

1. Module circuit ground is isolated from module chassis ground with in the module.
2. Open collector; should be pulled up with 4.7k-10k ohms on host board to a voltage between 3.15V and 3.6V.

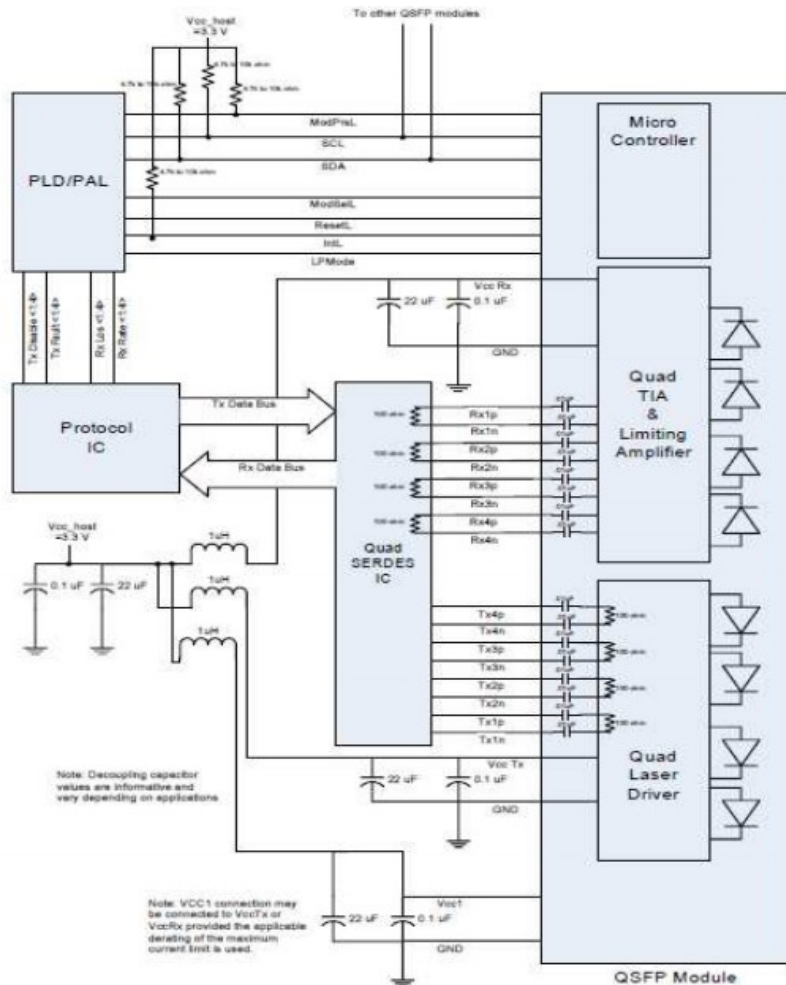
**Electrical Pin-out Details**



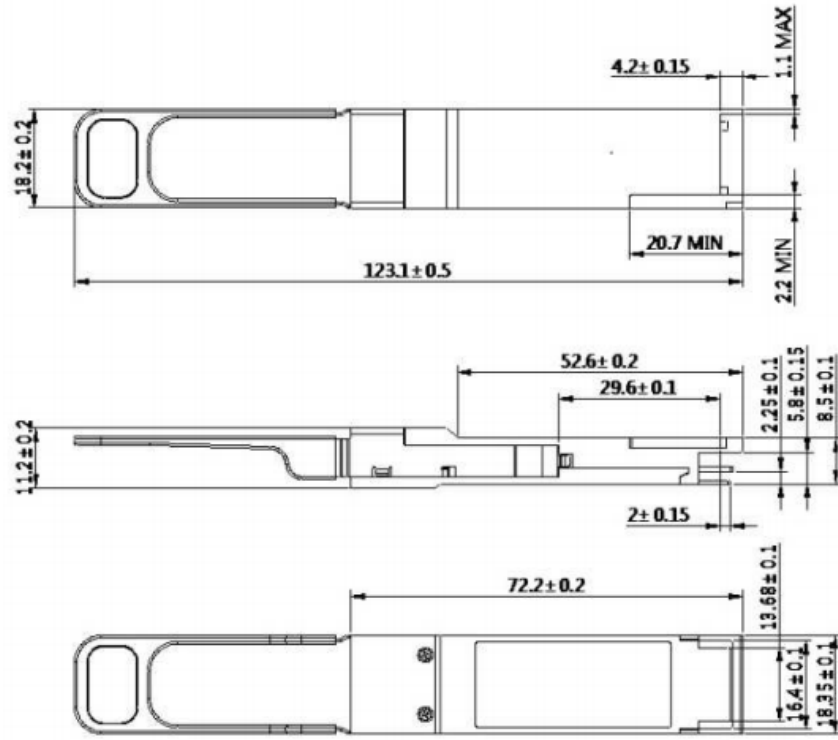
### Recommended Host Board Power Supply Filter Network



### Recommended Application Interface Block Diagram



# Mechanical Specifications



**About Us:**

Proline Options is one of North America's leading providers of transceivers and high speed cabling. With a reputation for quality, tested products that cover the connectivity spectrum, Proline Options has a solution for you regardless of the specification.

At Proline Options, every product is tested in its intended application - never batch or spec tested only. We run bandwidth, distance and IOS network tests. We have documented an impressive 0.03% failure rate over the last 10 years. To continue this rate of success we invest millions annually in our own on-site testing lab.



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